

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	486390	(connector substrate carrier board pcb) with (cavity opening groove)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/06 19:35
L2	84340	(chip die dice semiconductor ic (integrated adj circuit) component electronic) with 1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/06 19:36
L3	25021	(contact terminal electrode pad tab) with 2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/06 19:38
L4	4161	(lead leadframe trace pin conductor) with 3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/06 19:39
L5	781	(card mobil telephone phone digital) and 4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/06 19:40
L6	1137	(decoupl\$4 capacit\$4) and 4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/06 19:41
L7	309	5 and 6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/06 19:41